L	Hits	Search Text	DB	Time stamp
Number				
1	144	(multi-bit adj3 symbol\$1) or (multiple-bit adj3 symbol\$1)	USPAT; EPO; JPO; DERWENT	2004/04/22 11:06
2	35	((multi-bit adj3 symbol\$1) or	USPAT;	2004/04/22
		(multiple-bit adj3 symbol\$1)) and (ECC or (error adj2 correction adj2 code\$1))	EPO; JPO; DERWENT	11:07
4	109	(((multi-bit adj3 symbol\$1) or (multiple-bit adj3 symbol\$1 or n-bit\$1))	USPAT; EPO; JPO;	2004/04/22
		near10 stor\$3)and (ECC or (error adj2 correction adj2 code\$1))	DERWENT	
3	2	(((multi-bit adj3 symbol\$1) or	USPAT;	2004/04/22
		<pre>(multiple-bit adj3 symbol\$1)) near10 stor\$3)and (ECC or (error adj2 correction adj2 code\$1))</pre>	EPO; JPO; DERWENT	11:14
5	1	((multi-bit adj3 symbol\$1) or	USPAT;	2004/04/22
		(multiple-bit adj3 symbol\$1)) and (solid adj2 state adj2 stor\$3)and (ECC or (error	EPO; JPO; DERWENT	11:14
		adj2 correction adj2 code\$1))		
6	10	((multi-bit\$1) or (multiple-bit\$1)) and (solid adj2 state adj2 stor\$3)and (ECC or	USPAT; EPO; JPO;	2004/04/22
		(error adj2 correction adj2 code\$1))	DERWENT	
7	223	((multi-bit\$1) or (multiple-bit\$1)) and ((solid adj2 state adj2 stor\$3) or	USPAT; EPO; JPO;	2004/04/22
		(storage adj2 device) or (secondary adj2	DERWENT	
		<pre>memroy) or (mass adj2 storage)) and (ECC or (error adj2 correction adj2 code\$1))</pre>		
8	3	((multi-bit\$1) or (multiple-bit\$1)) and	USPAT;	2004/04/22
		((solid adj2 state adj2 stor\$3) or (storage adj2 device) or (secondary adj2	EPO; JPO; DERWENT	11:23
		memroy) or (mass adj2 storage)) and (ECC		
9	6	<pre>adj2 encoder) ((multi-bit\$1) or (multiple-bit\$1)) and</pre>	USPAT;	2004/04/22
		((solid adj2 state adj2 stor\$3) or	EPO; JPO;	11:24
		(storage adj2 device) or (secondary adj2 memroy) or (mass adj2 storage)) and (ECC	DERWENT	
10	9	<pre>adj2 decoder) ((multi-bit\$1) or (multiple-bit\$1)) and</pre>	IICDAT.	2004/04/22
10	9	((solid adj2 state adj2 stor\$3) or	USPAT; EPO; JPO;	11:24
		(storage adj2 device) or (secondary adj2 memroy) or (mass adj2 storage)) and (ECC	DERWENT	
		near2 encod\$3)		
11	30	<pre>(multi-bit\$1 or multiple-bit\$1 or eight-bit\$1 or 8-bits) and ((solid adj2</pre>	USPAT; EPO; JPO;	2004/04/22
		state adj2 stor\$3) or (storage adj2	DERWENT	11.45
		device) or (secondary adj2 memroy) or (mass adj2 storage)) and (ECC near2		
		encod\$3)		
12	21	<pre>(multi-bit\$1 or multiple-bit\$1 or n-bit\$1) and ((solid adj2 state adj2</pre>	USPAT;	2004/04/22
		stor\$3) or (storage adj2 device) or	EPO; JPO; DERWENT	11:70
		(secondary adj2 memroy) or (mass adj2 storage)) and (ECC near2 encod\$3)		
13	21	(multi-bit\$1 or multiple-bit\$1 or	USPAT;	2004/04/22
		n-bit\$1) and ((solid adj2 state adj2	EPO; JPO;	11:26
		stor\$3) or (storage adj2 device) or (secondary adj2 memroy) or (mass adj2	DERWENT	
		storage)) and (ECC near2 encod\$3) and Reed-Solomon		
14	14	(multi-bit\$1 or multiple-bit\$1 or	USPAT;	2004/04/22
		n-bit\$1) and ((solid adj2 state adj2	EPO; JPO;	11:26
		stor\$3) or (storage adj2 device) or (secondary adj2 memroy) or (mass adj2	DERWENT	
		storage)) and (ECC near2 encod\$3) and		
		Reed-Solomon and controller		1

15	19		USPAT;	2004/04/22
		n-bit\$1) and ((solid adj2 state adj2	EPO; JPO;	11:27
		stor\$3) or (storage adj2 device) or	DERWENT	ļ
		(secondary adj2 memroy) or (mass adj2		
		storage)) and (ECC near2 encod\$3) and]	
		Reed-Solomon and control\$4		
16	0	(multi-bit\$1 or multiple-bit\$1 or	USPAT;	2004/04/22
		n-bit\$1) and ((solid adj2 state adj2	EPO; JPO;	11:28
		stor\$3) or (storage adj2 device) or	DERWENT	
		(secondary adj2 memroy) or (mass adj2		
		storage)) and (ECC near2 encod\$3) and		
		((single adj2 row) or (one adj2 row))		
17	21	((solid adj2 state adj2 stor\$3) or	USPAT;	2004/04/22
		(storage adj2 device) or (secondary adj2	EPO; JPO;	11:28
		memroy) or (mass adj2 storage)) and (ECC	DERWENT	
		near2 encod\$3) and ((single adj2 row) or	Ì	
		(one adj2 row))		